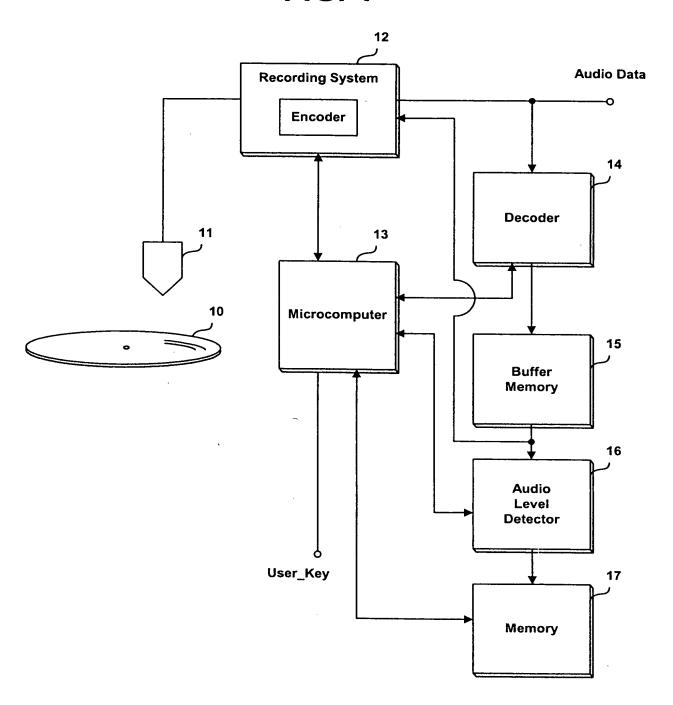
FIG. 1



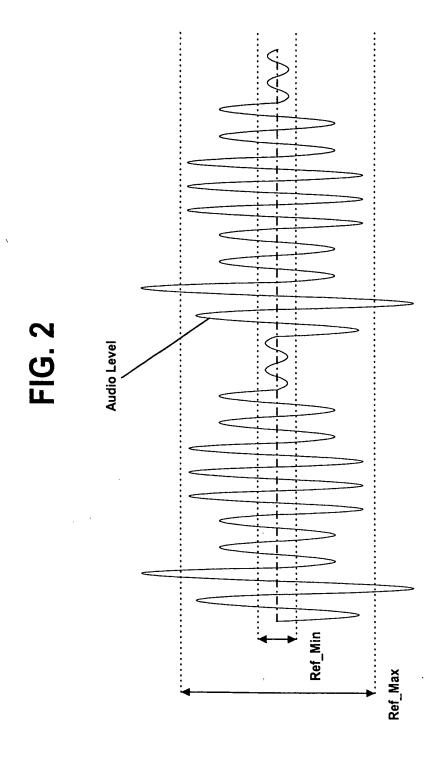


FIG. 3

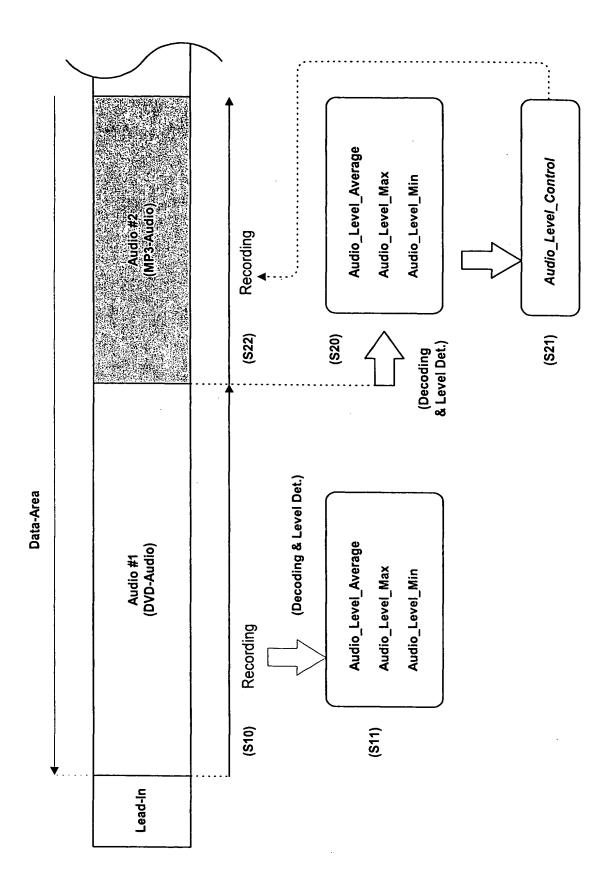


FIG. 4 Compressed Digital Audio Data 51 52 53 Level Buffer Control Memory

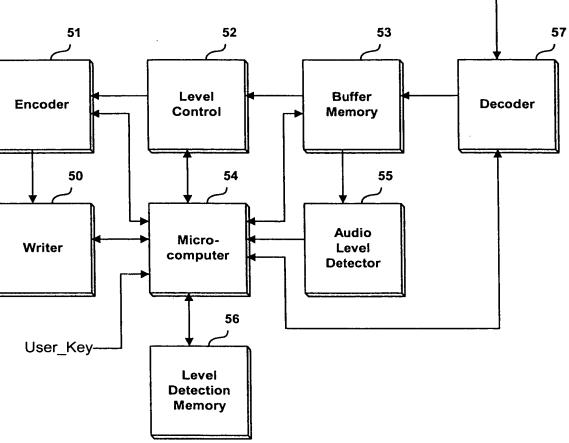


FIG. 5

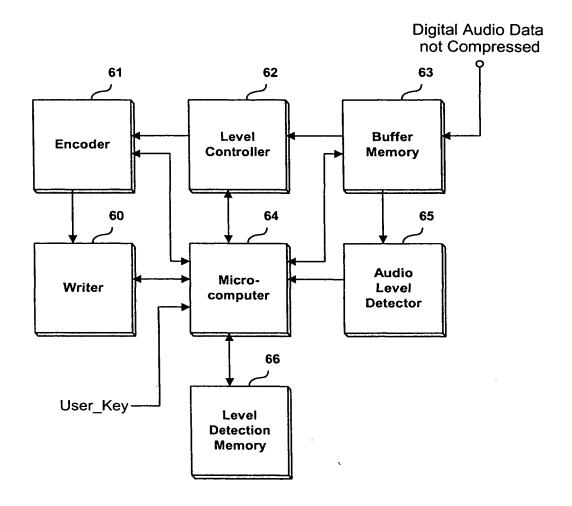


FIG. 6

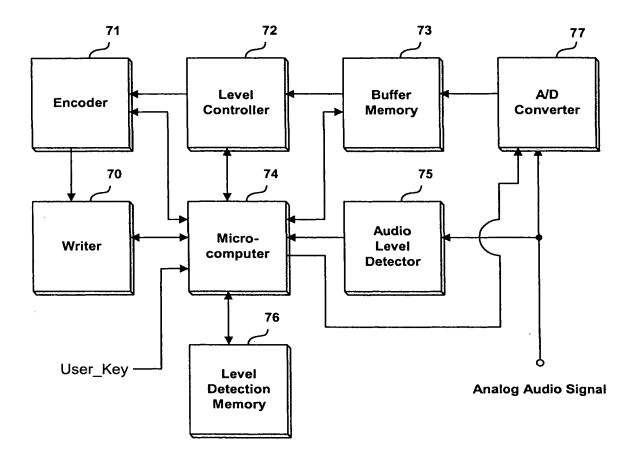


FIG. 7

